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INFORMATI	ON DISCLOSURI	Application Number	10/734.839		
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k	·	A Novel Phase Noise Reduction Technique in Oscillators Using Defected Ground Structure (IEEE Microwave and Wireless Componenets Letters, Vol. 12, No. 2, February 2002, Pages 39-41)		
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Examiner Signature	Joseph	Chang	Date Considered	5/4/05
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Application Number	10/734,839			
Filing Date	December 11, 2003			
First Named Inventor	Byung-Su Kang			
Art Unit				
Examiner Name				
Attorney Docket Number	51876P427			

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Examiner Initials*	Cite No.'	Document Number Number - Kind Code ² (if known)	Publication Date	Name of Patentiee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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22		JP 2002-217992	08-02-2002	Matsushita Electric Ind. Co. Ltd.		
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